

**Amendments to the Specification:**

Please delete paragraphs 22, 23, and 24.

Please amend paragraph 20 as indicated below:

[0020] In one embodiment, a branch aware first-in first-out (FIFO) memory is disclosed.

The branch aware FIFO ~~first-in first-out~~ memory includes a memory array to store data; a push pointer ~~coupled to the memory array~~ to address memory locations ~~therein~~ in the FIFO memory in order to write data; a pop pointer ~~coupled to the memory array~~ to address memory locations in the FIFO memory ~~therein~~ in order to read data; ~~and~~ a pointer memory ~~coupled to the pop pointer~~; and control logic coupled to the pointer memory. The pointer memory saves one or more prior pop pointer values of the pop pointer. The control logic may restore one of the one or more prior pop pointer values from the pointer memory into the pop pointer in response to receiving branch information.

Please replace the abstract with the replacement abstract provided on the next, separate sheet.